

FORM PTO-1449		ATTY. DOCKET NO.	SERIAL NO.
		HP10981470-1	09/444173
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		APPLICANT	
(Use several sheets if necessary)		Fong Pong	
		FILING DATE	GROUP
		11/19/1999	2187

1C530 U.S. 444173
1/19/99

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1A					
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Tving, "Multiprocessor Interconnection Using SCI," Master Thesis, DTH ID-E 579, 1993, pp. i-18.
1R	Brown, "Asynchronous Multicaches," Distributed Computing, 1990, 4:31-36.
1S	Afek et al., "A Lazy Cache Algorithm," ACM, 1989, pp. 209-222.

EXAMINER

DATE CONSIDERED

Jamie Song

5/3/01

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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

2Q	Afek et al., "Lazy Caching," ACM Transactions on Programming Languages and Systems, Vol. 15, No. 1, January 1993, pp. 182-205.
2R	Patterson et al., "Centralized Shared-Memory Architectures," Computer Architecture A Quantitative Approach, Morgan Kaufmann Publishers, Inc., San Francisco, CA, chapter 8, section 8.3, pp. 654-693, 1990.
2S	

EXAMINER	DATE CONSIDERED
<i>Jamie S</i>	5/3/01